

09/059,644 (H4) Sheet 1 of 2

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-898		PRIORITY SERIAL NO. 08/710,353			
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Micron Technology, Inc.		PRIORITY FILING DATE 09/17/96		PRIORITY GROUP H07 2872	
U.S. PATENT DOCUMENTS									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
mt	AA	4,971,655	11/20/90	Stefano et al.	156	659.1	12/26/89		
	AB	4,981,810	1/1/91	Fazan et al.	437	44	2/16/90		
	AC	5,286,344	2/15/94	Blalock et al.	156	657	6/15/92		
	AD	5,290,720	3/1/94	Chen	437	41	7/26/93		
	AE	5,306,951	4/26/94	Lee et al.	257	755	5/14/92		
	AF	5,334,556	8/2/94	Guldi	437	248	3/23/93		
	AG	5,420,800	5/30/95	Fukui	364	491	9/2/94		
	AH	5,439,846	8/8/95	Nguyen et al.	437	187	12/17/93		
	AI	4,786,609	11/1988	Chen	438	303			
	AJ	5,126,283	6/1992	Pintchovski	438	396			
mt	AK	5,371,026	12/1994	Hayden	438	275			
FOREIGN PATENT DOCUMENTS									
		Document Number	Date	Country	Class	Subclass	Translation		
							Yes	No	
	AL								
	AM								
	AN								
	AO								
	AP								
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)									
mt	AR		S. Wolf, et al., Silicon Processing For The VLSI Era, Lattice Press (1986), pp. 208 and 212, (month not available)						
	AS								
	AT								
EXAMINER Michael Trinh				DATE CONSIDERED 9/8/99					
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mt	AC	5,637,514	6/1997	Jeng	438	163	
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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EXAMINER

Michael Trinh

DATE CONSIDERED

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